

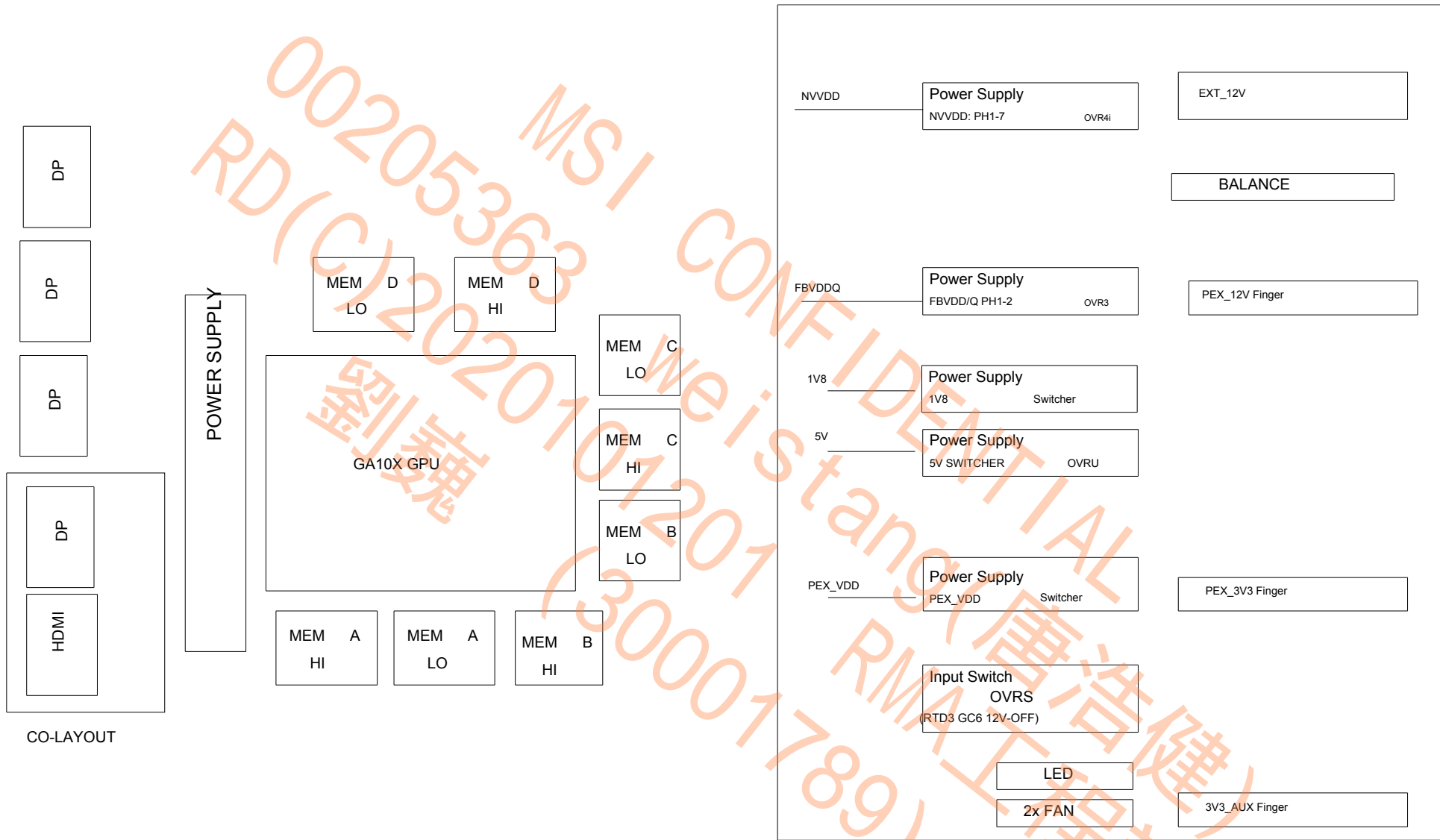
PG142-B00
DP + DP + DP + HDMI/DP

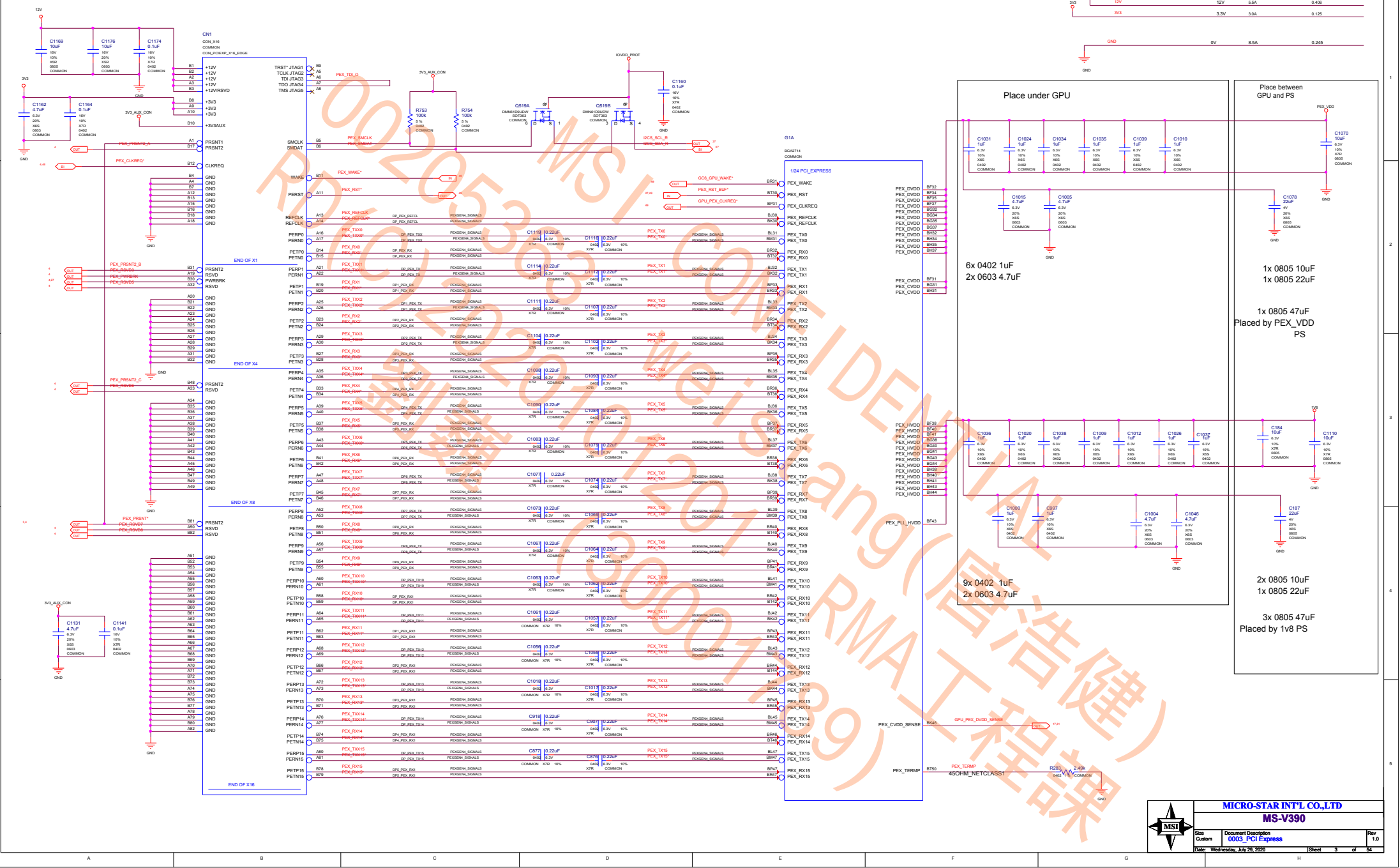
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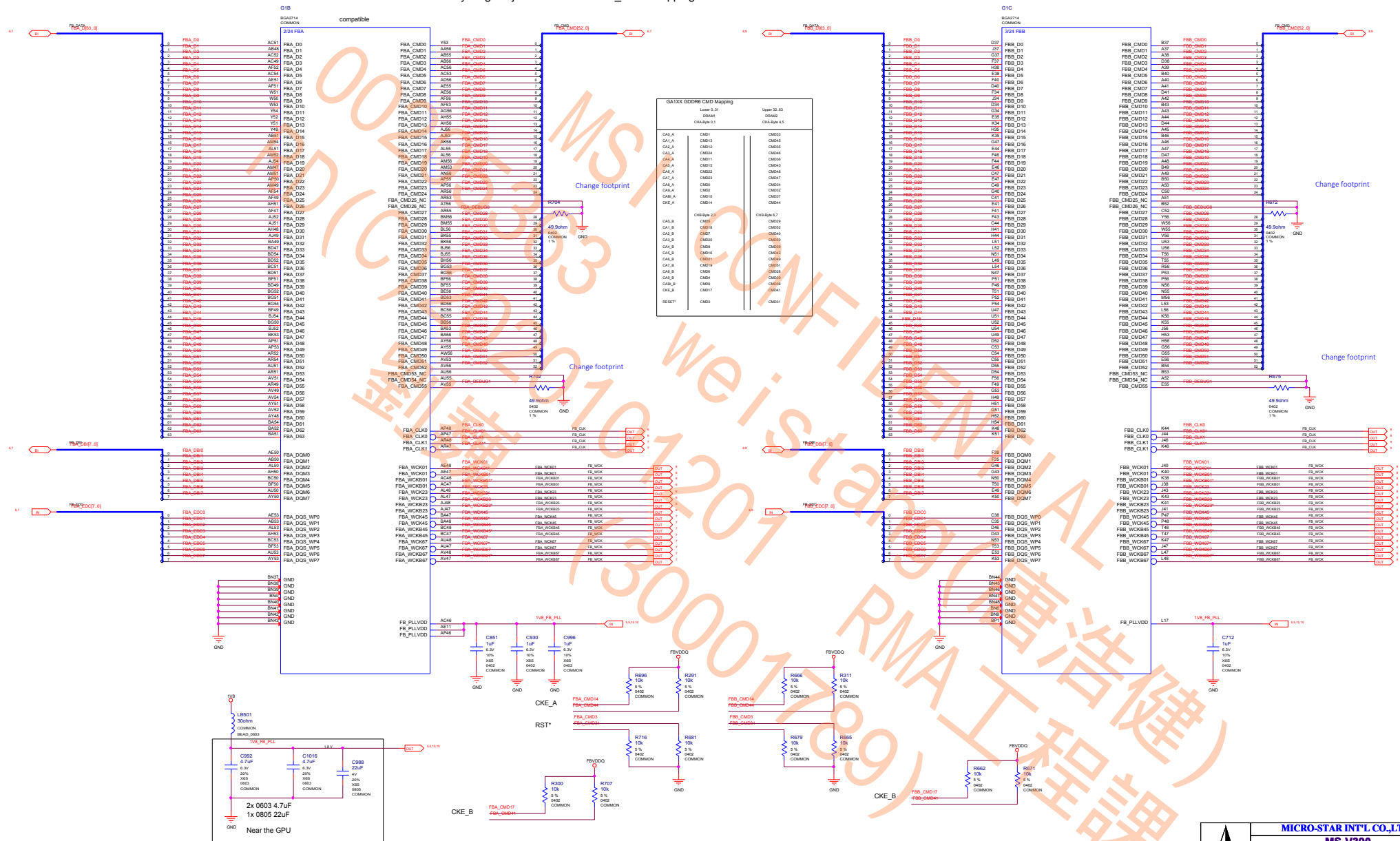
Page	Description
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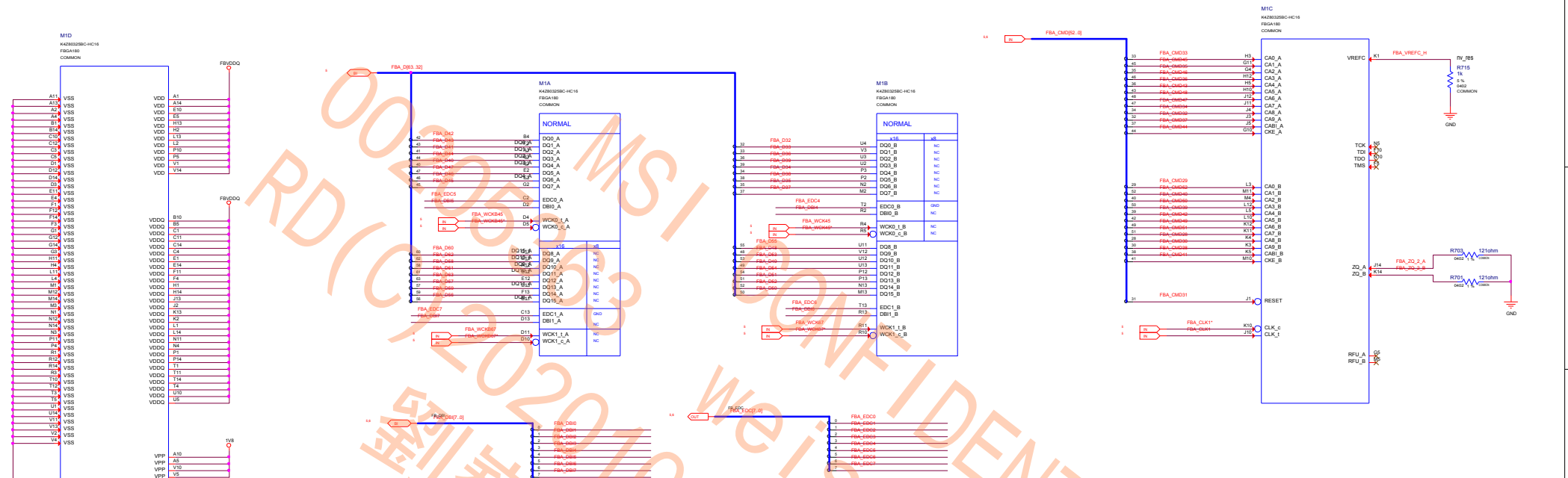




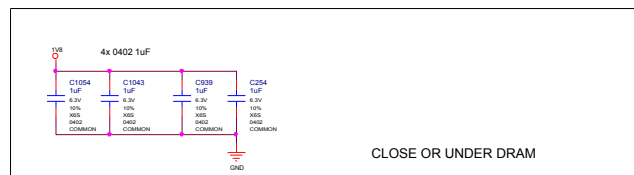
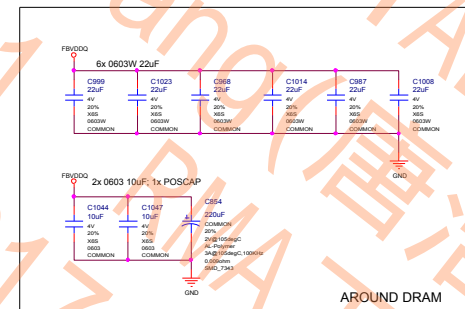
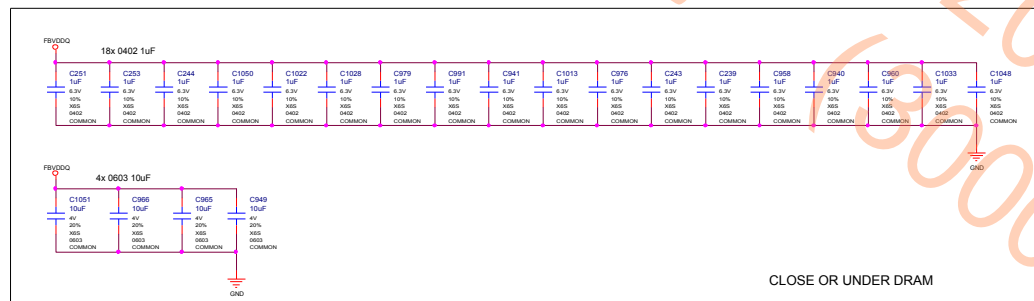


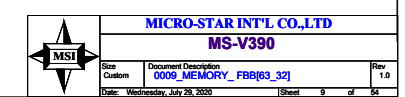
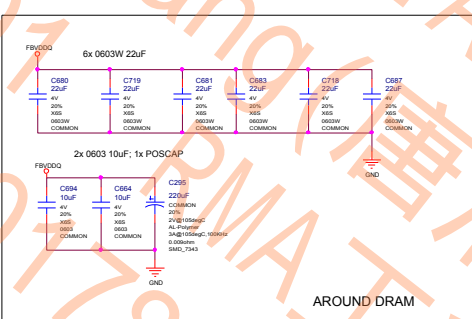
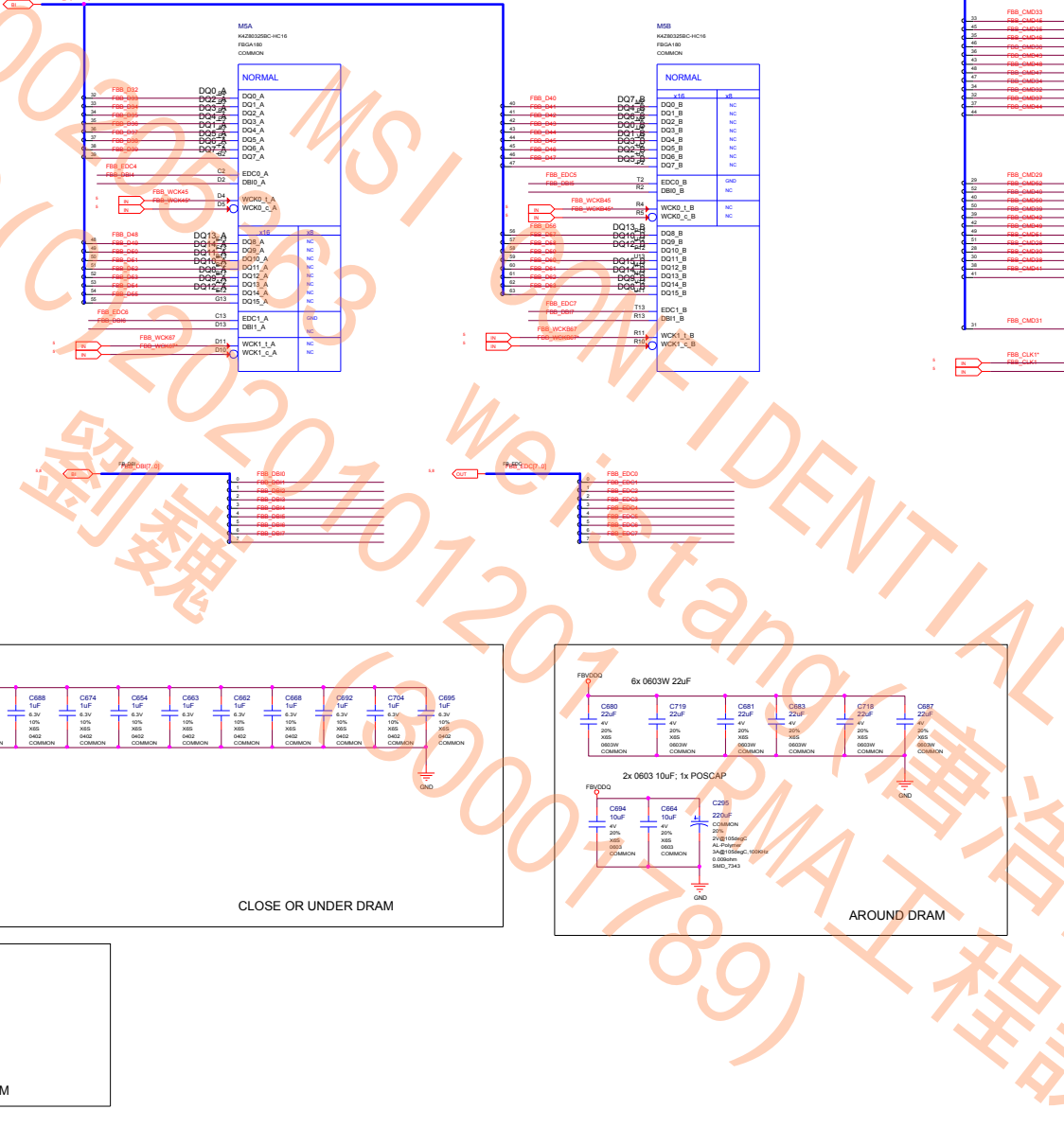
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//syseng/Projects/GA1xx/GA1XX_CMD-Mapping.xlsx
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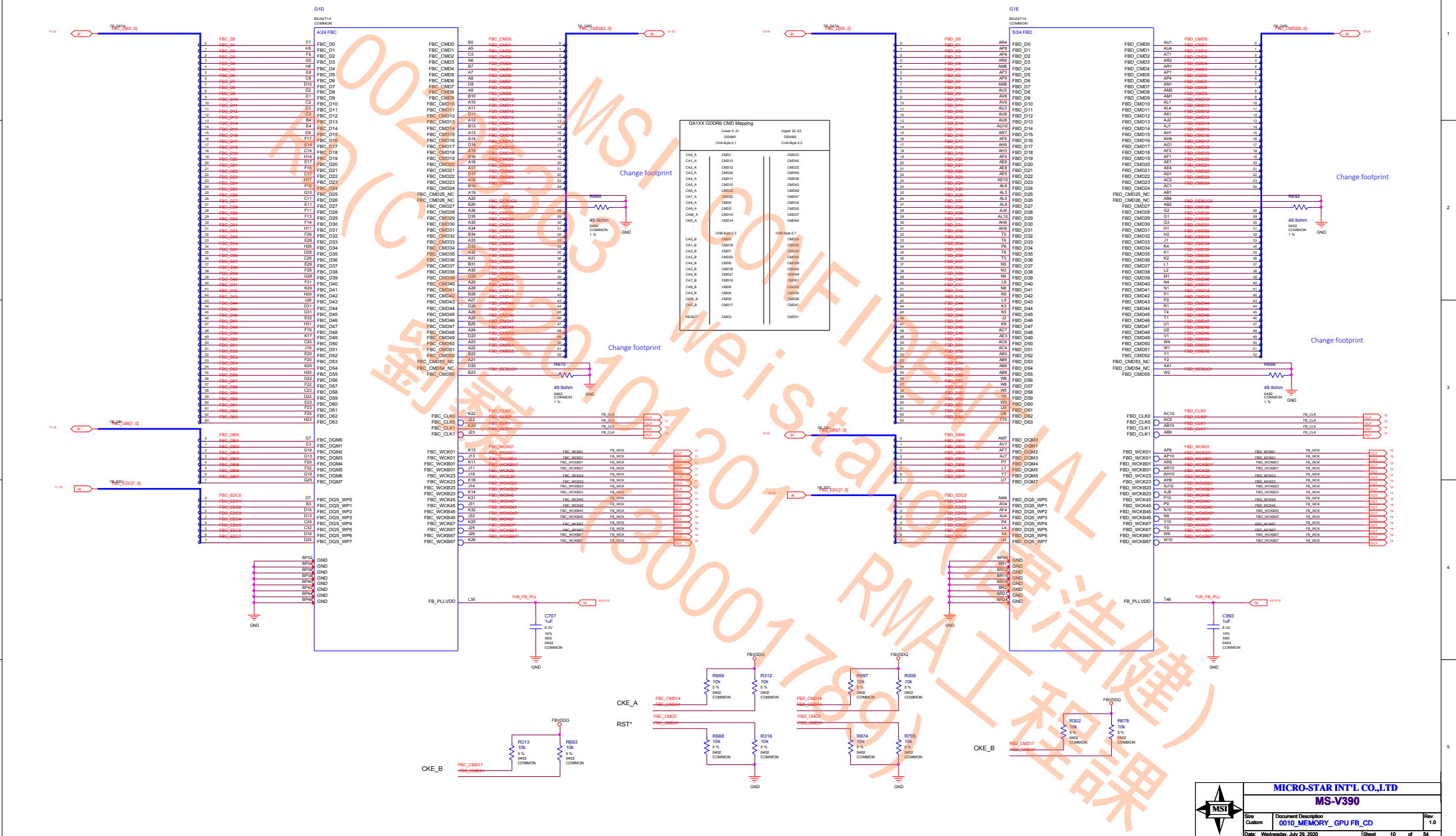


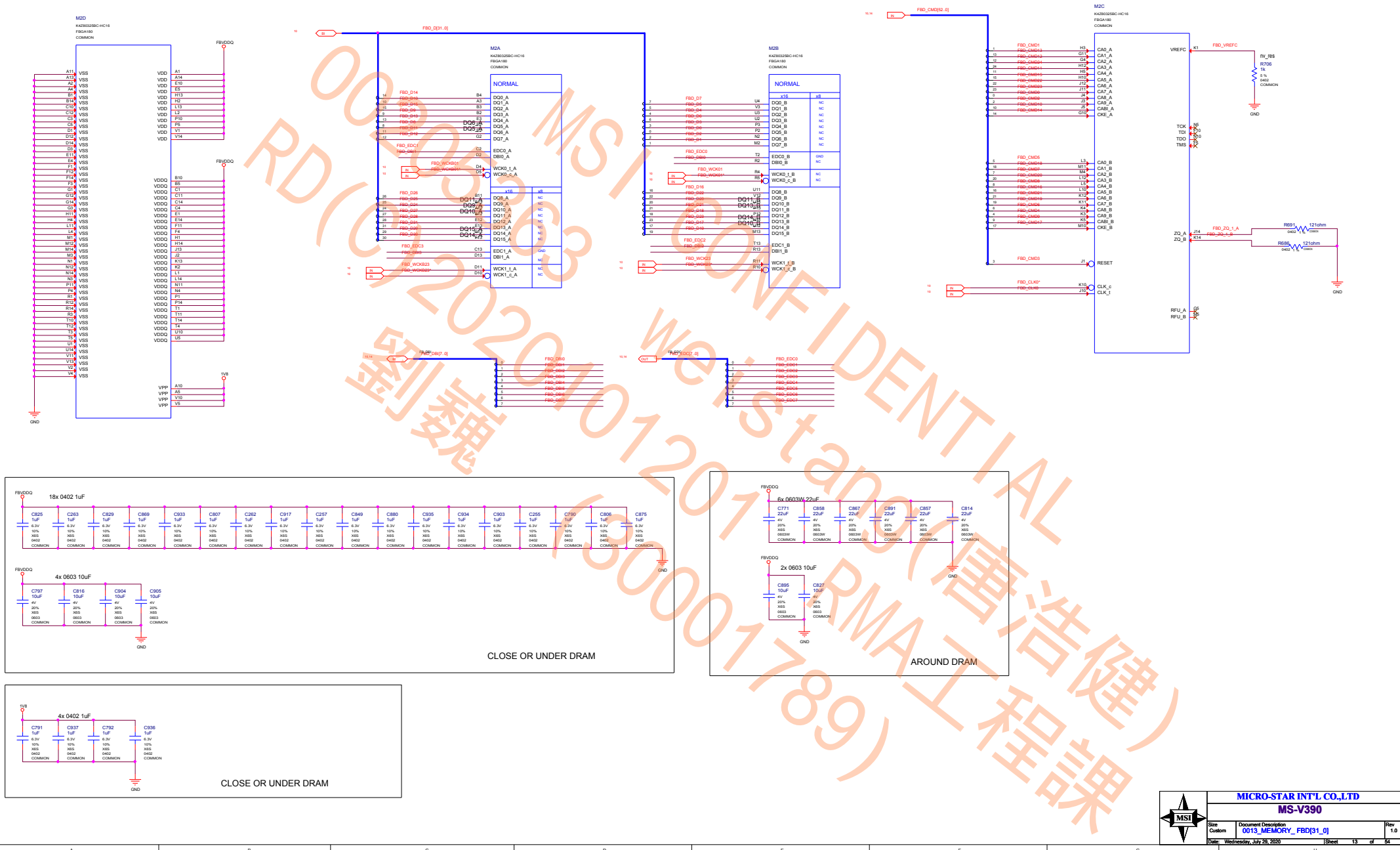


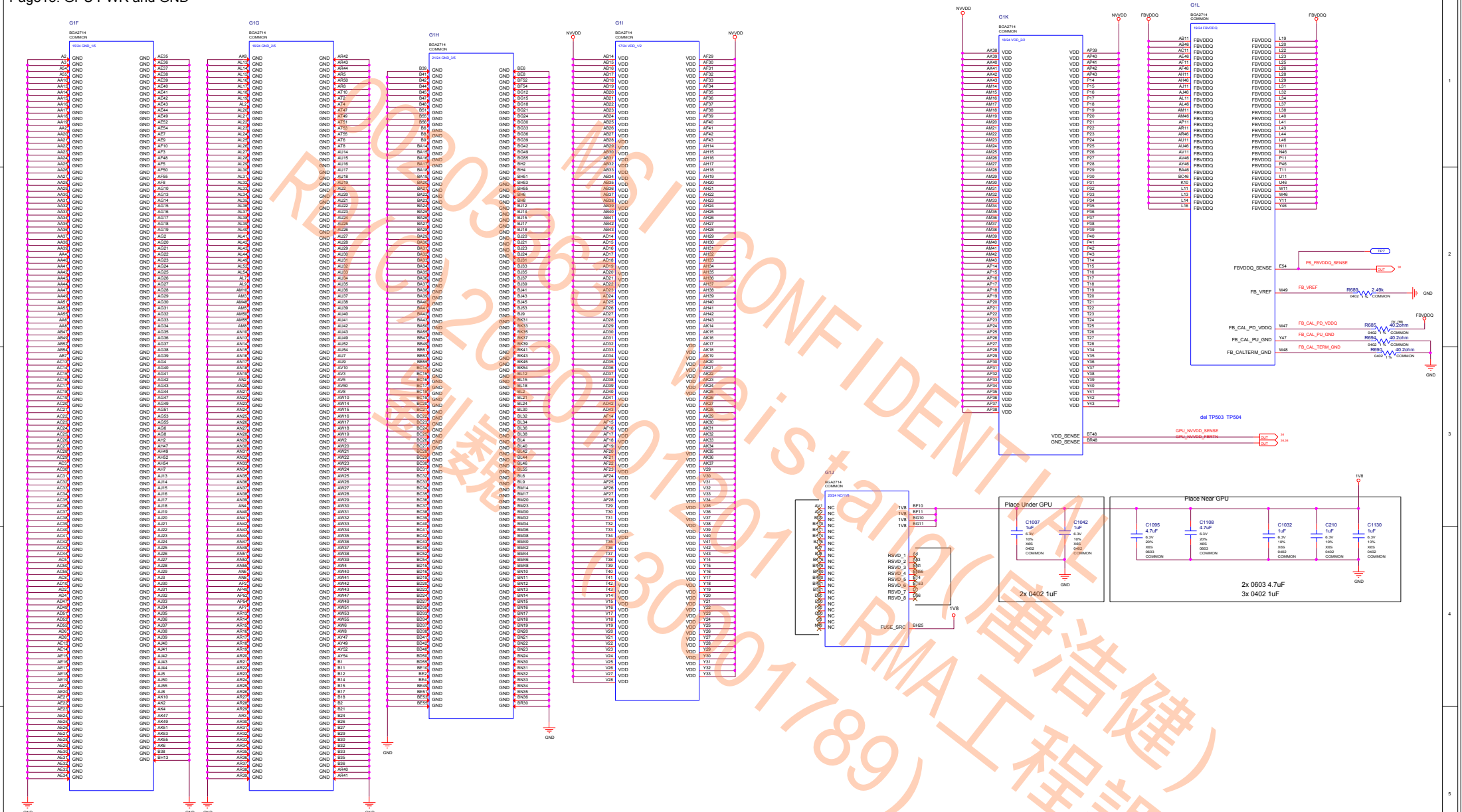
C239
location ---- colse remote sense

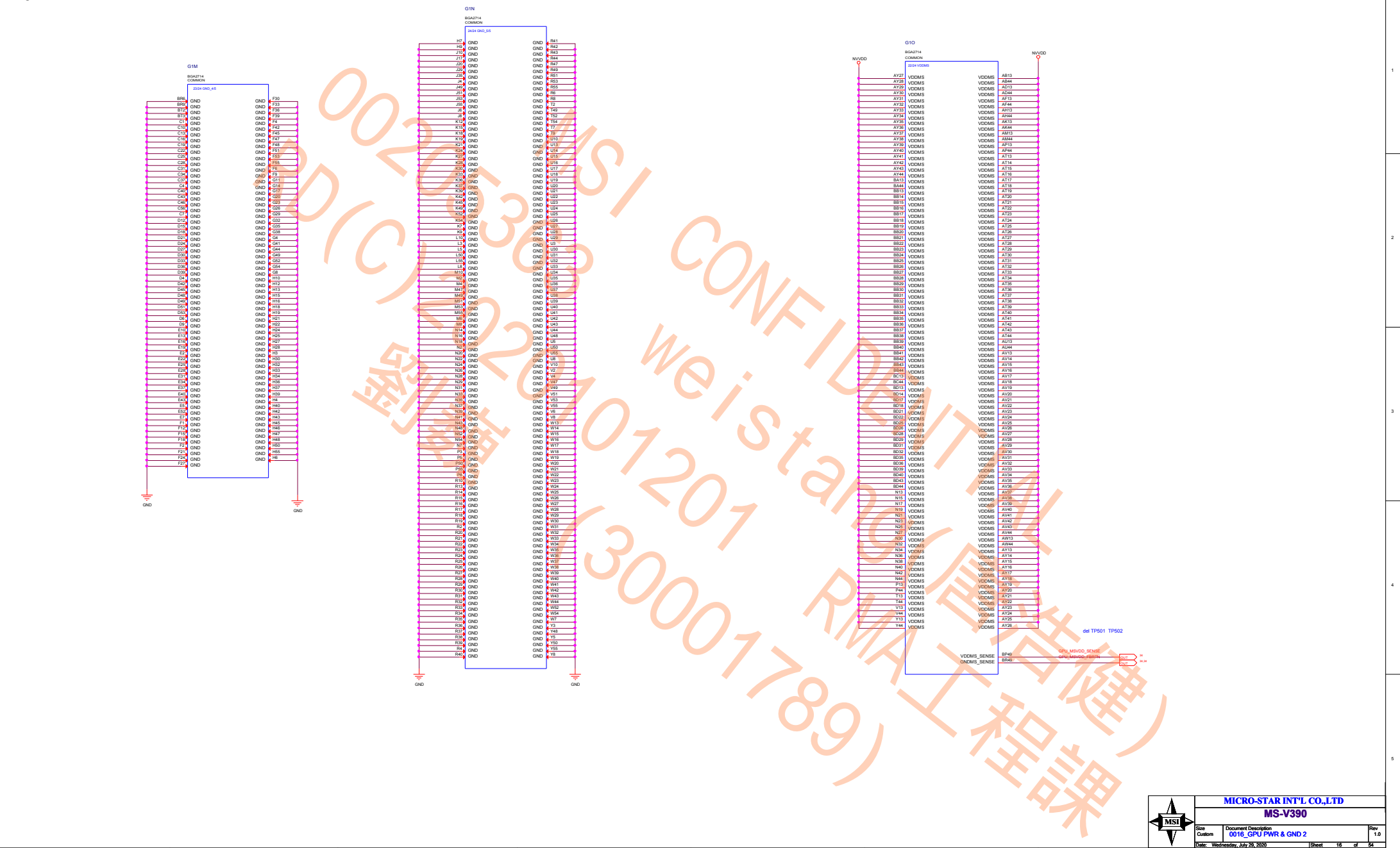


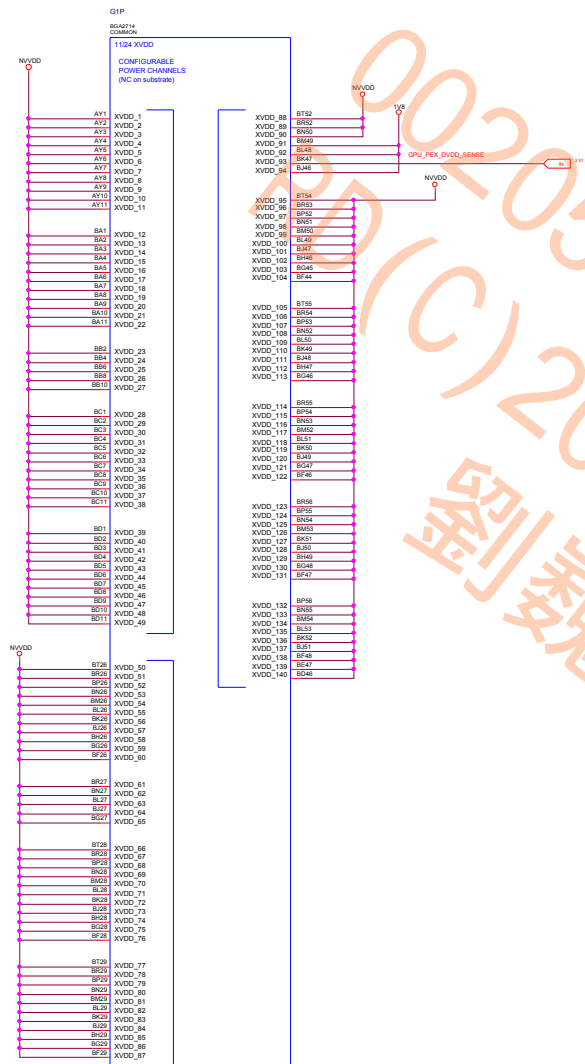






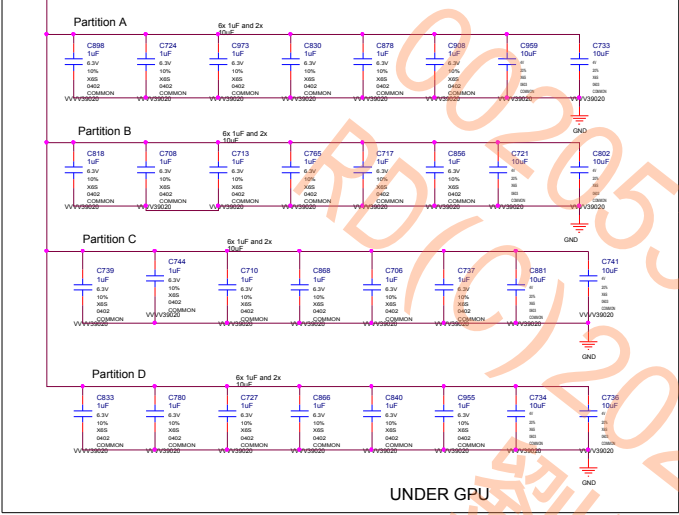






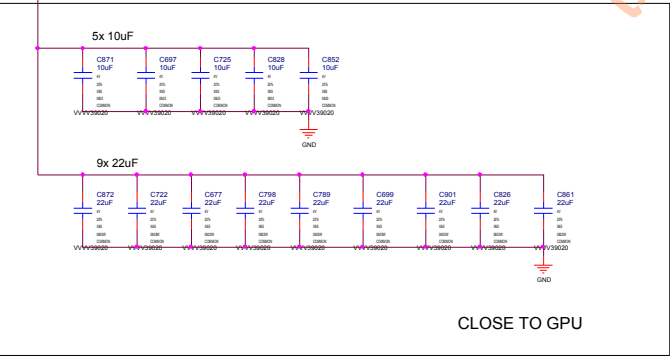
FBVDDQ

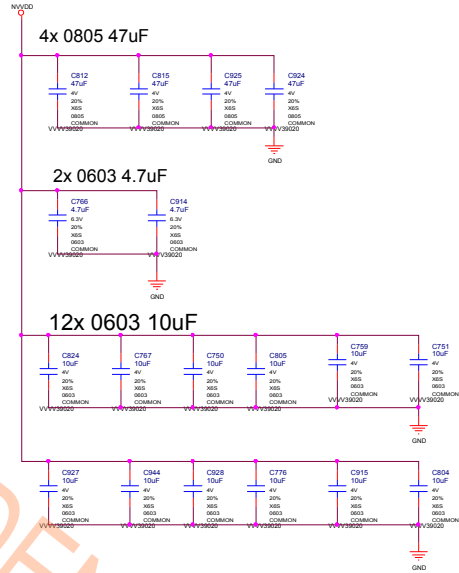
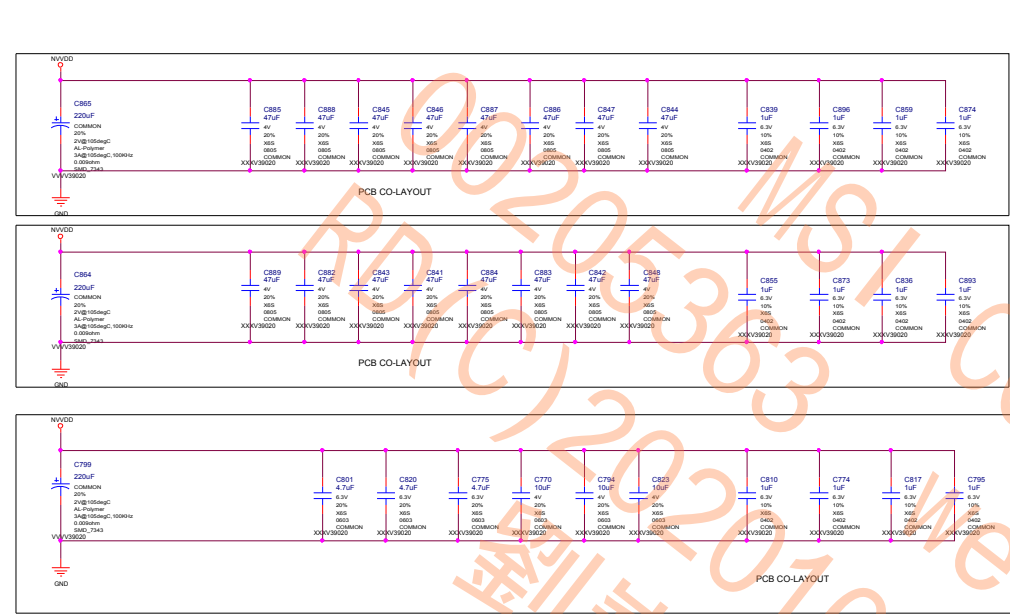
UNDER GPU: 24x 0402 1uF; 8x 0603 10uF



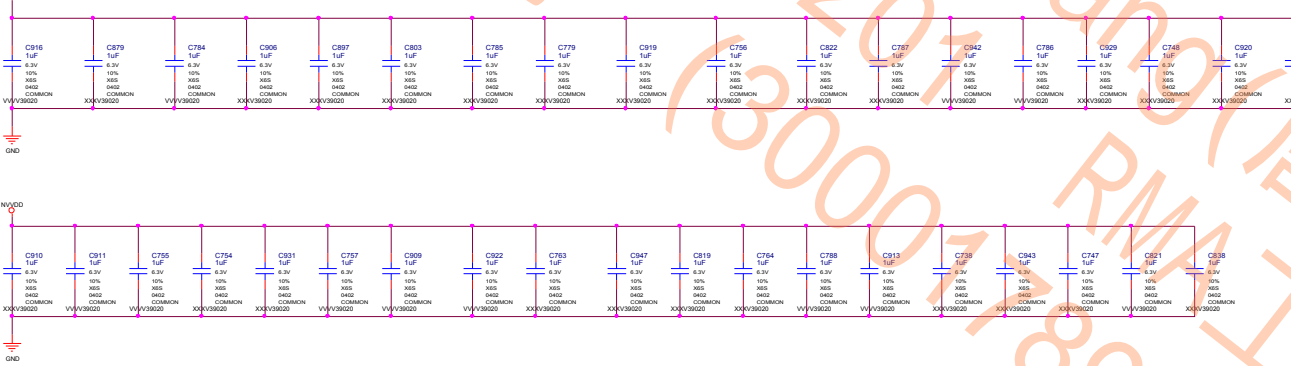
FBVDDQ

5x 0603 10uF; 9X 0603 22uF

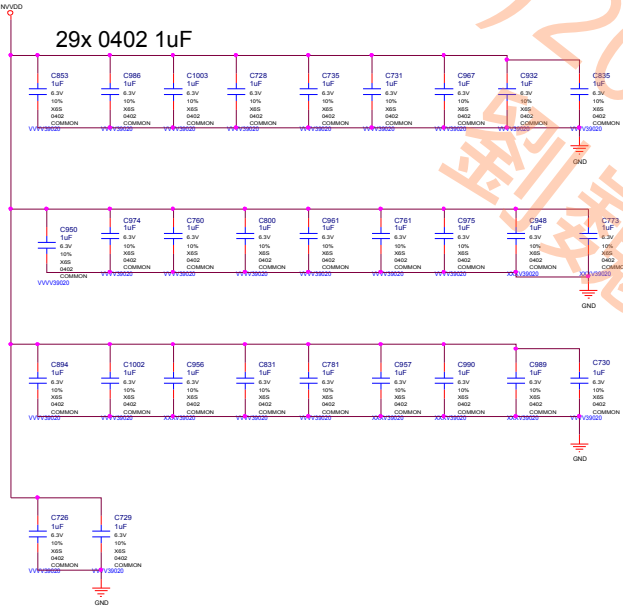
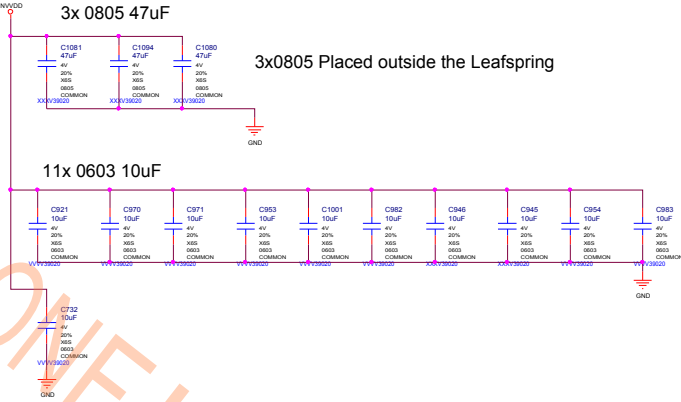
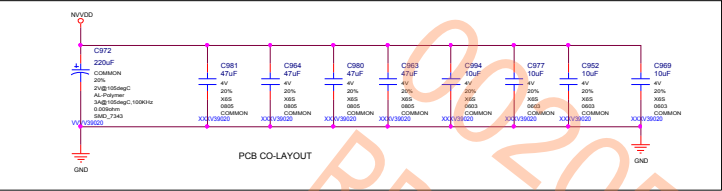




37 x 0402 1uF



UNDER GPU



UNDER GPU

WHITE DRIVE

Remove RGB LED

LOGO LED WHITE

LOGO LED RED

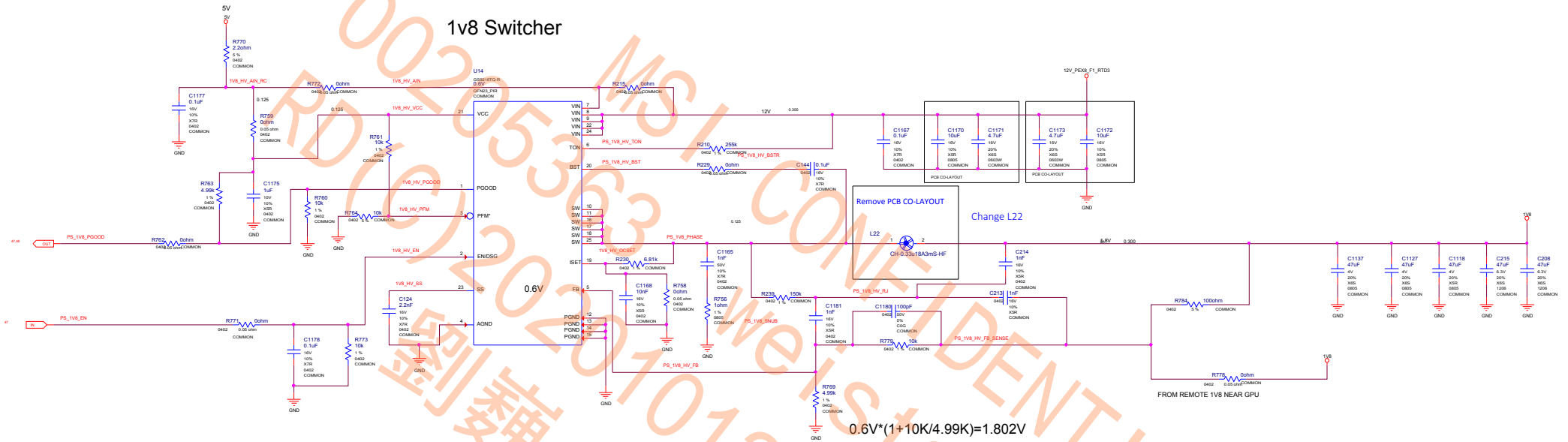
RED DRIVE

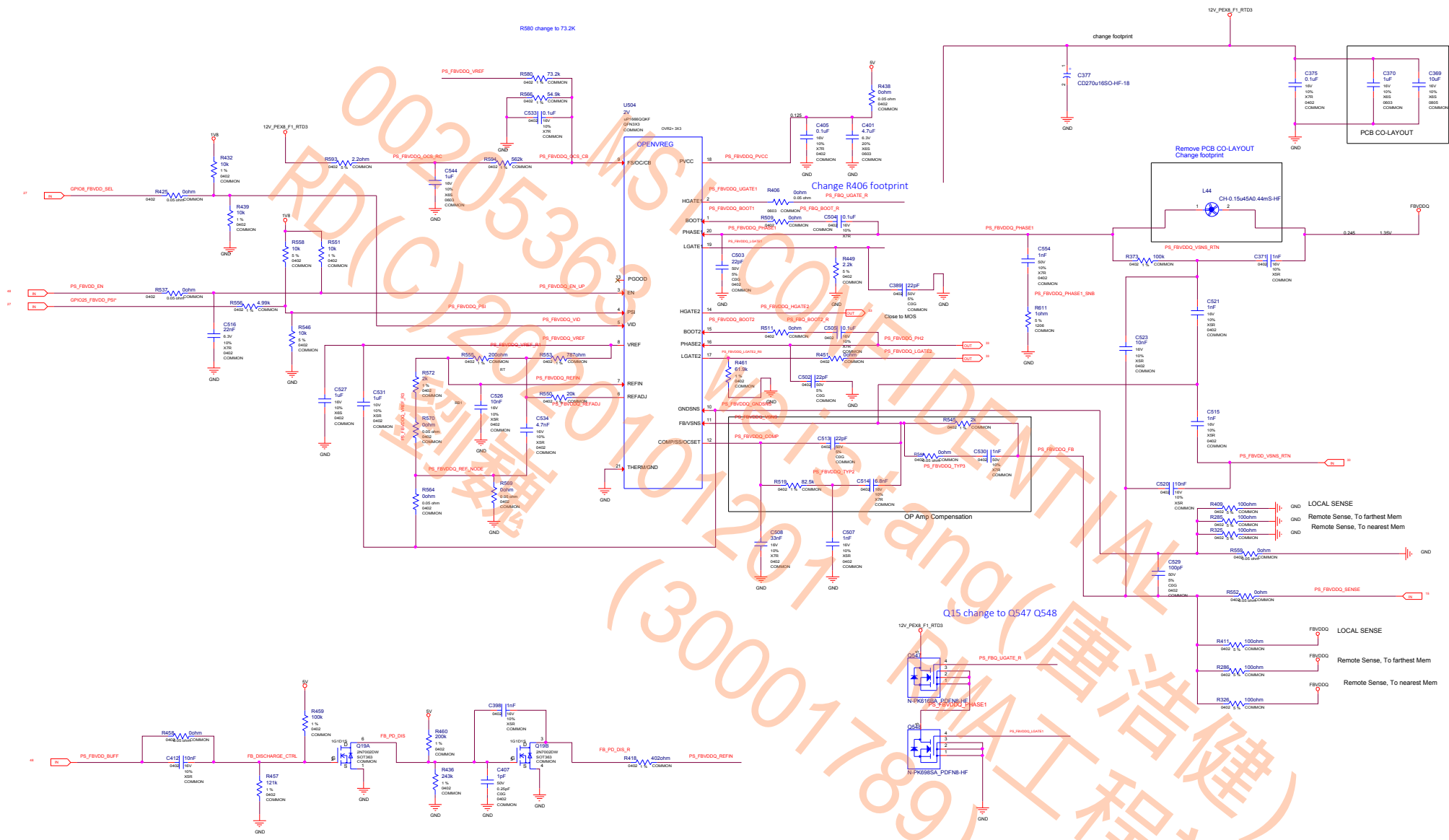
GREEN DRIVE

BLUE DRIVE

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	MS-V390		
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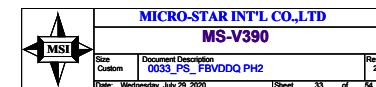
1v8 Switcher

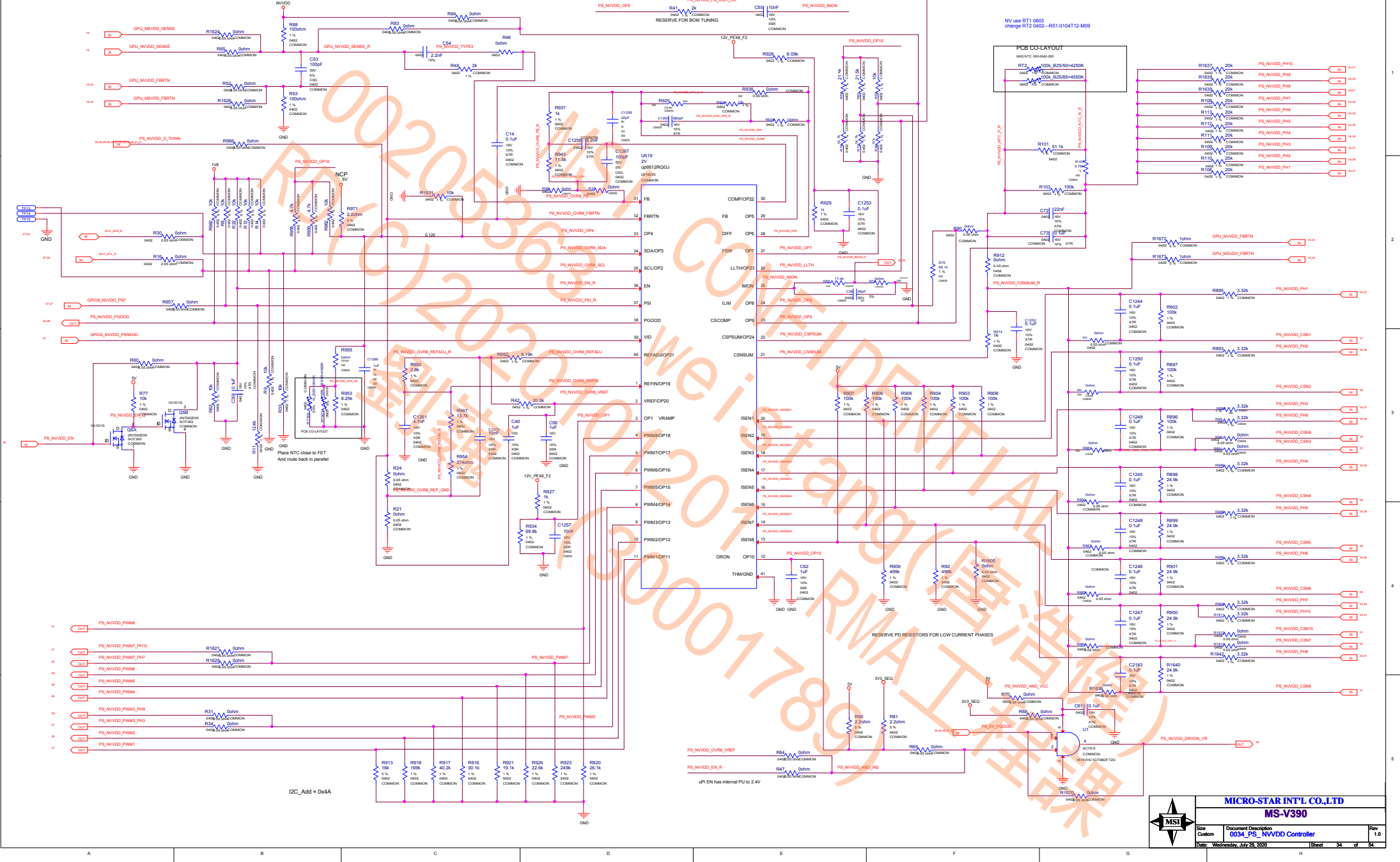





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MS-V390

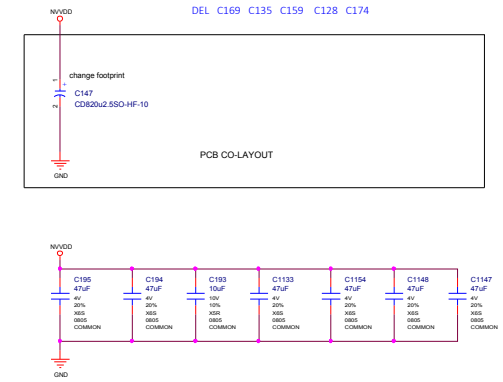
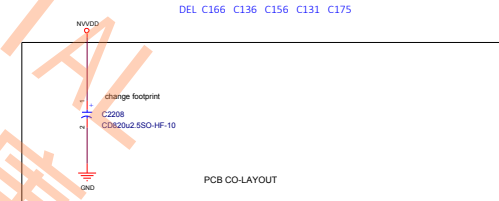
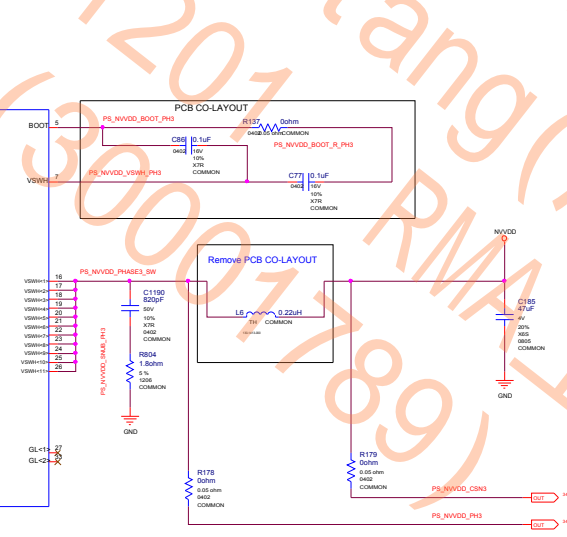
Size Custom	Document Description 0032_PS_FBVDDQ	Rev 1.0
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	MS-V390		
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[illegible]

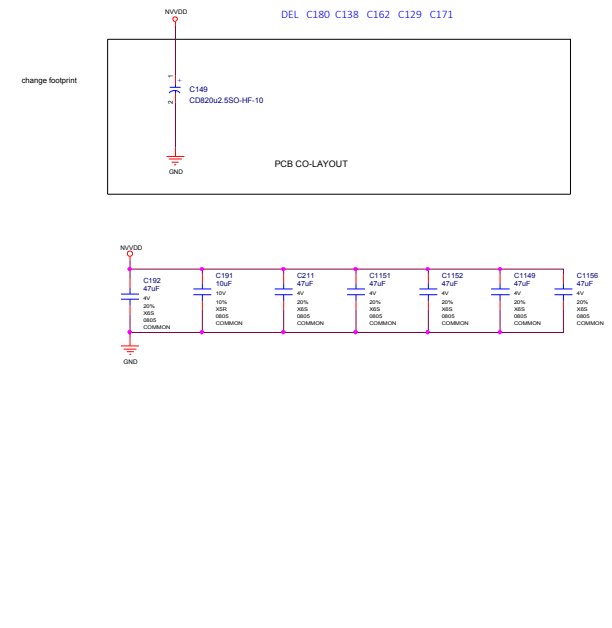
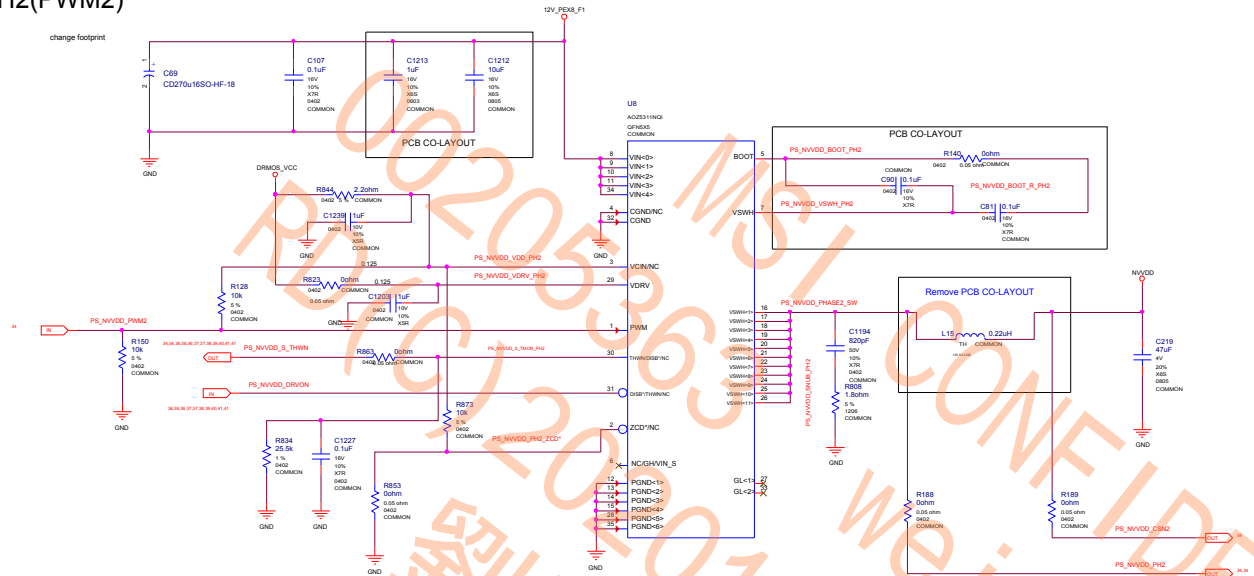
colayout only no self standing mlccs



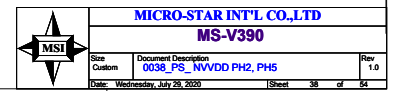
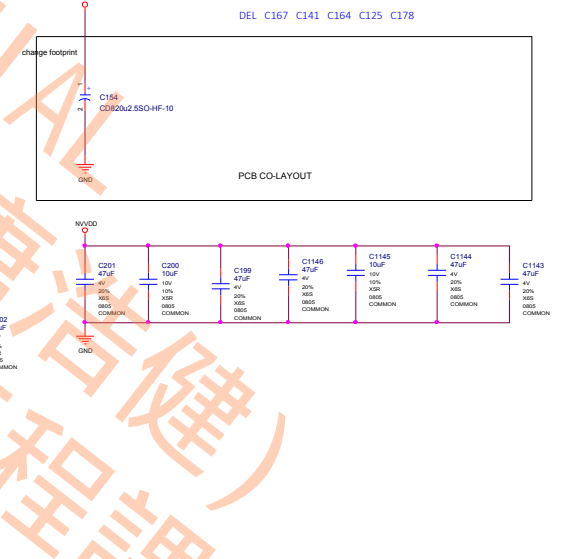
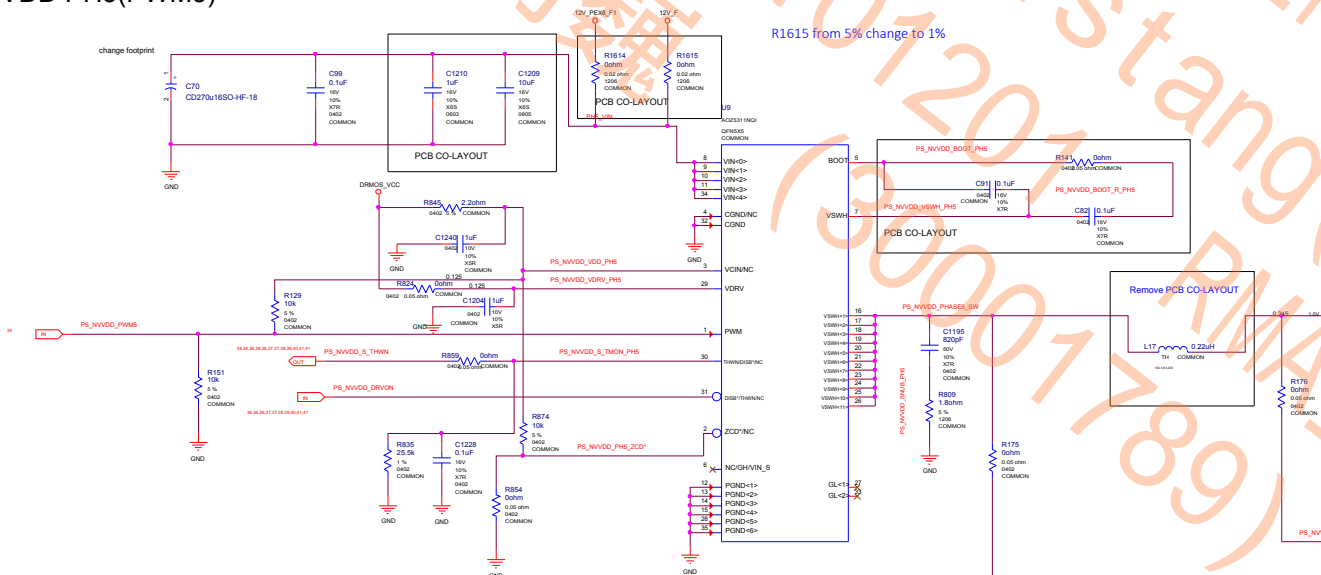
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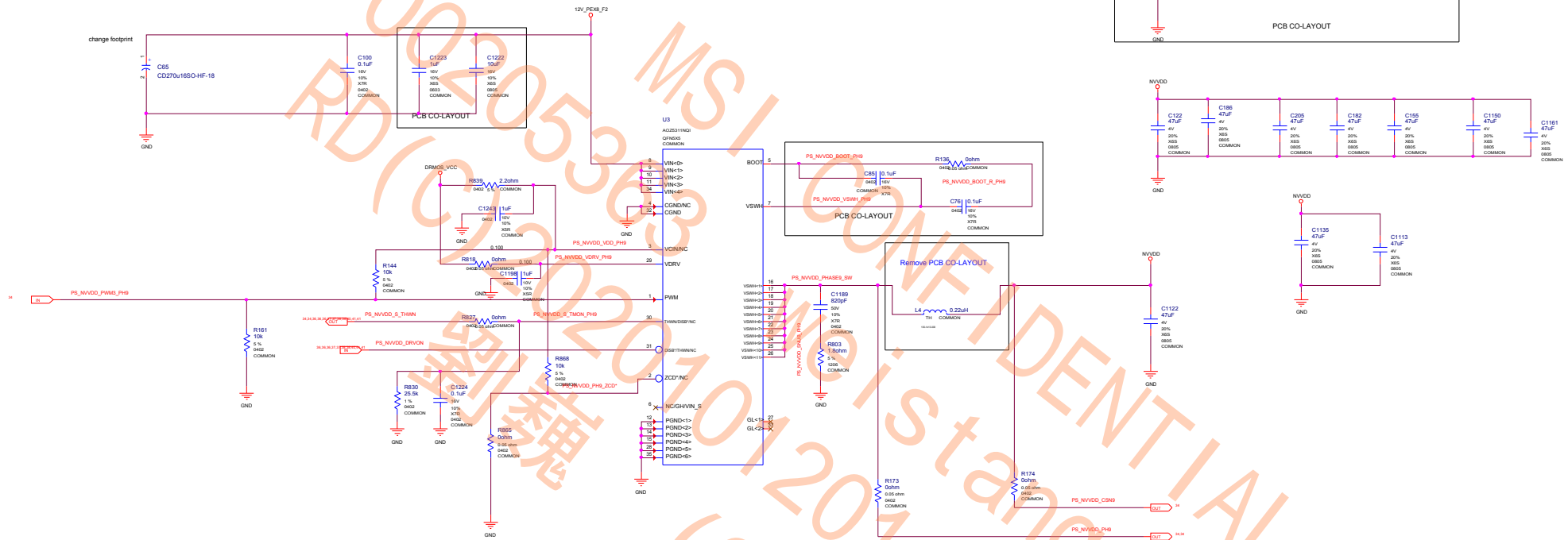
NVVDD PH2(PWM2)

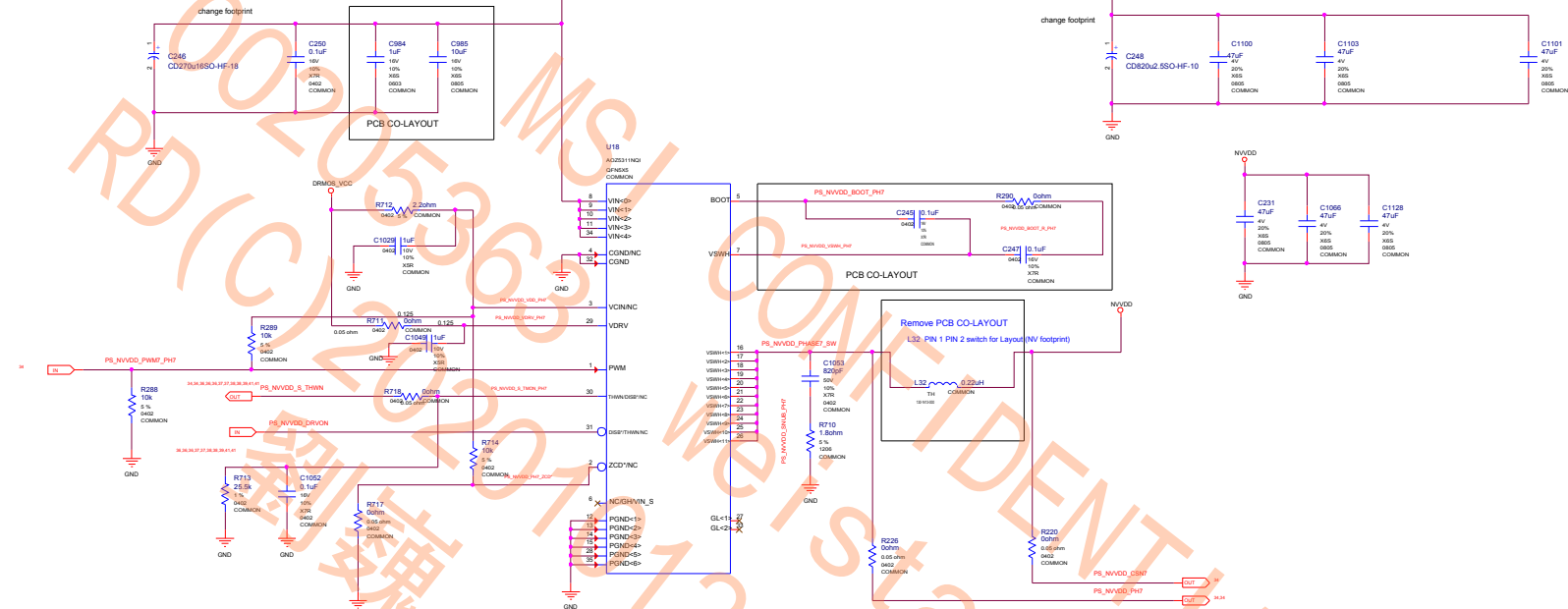


NVVDD PH5(PWM5)

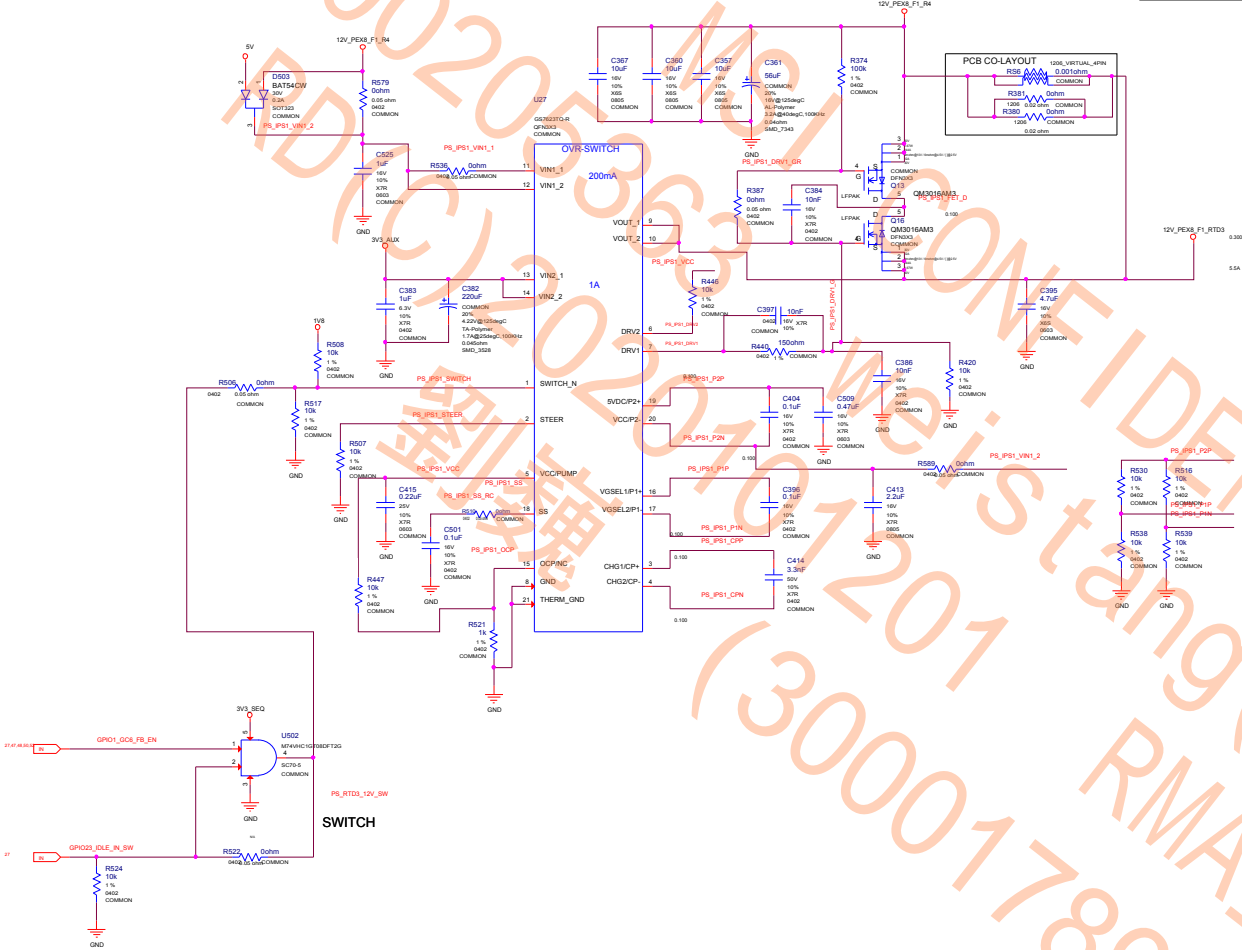


NVVDD PH9(PWM3)





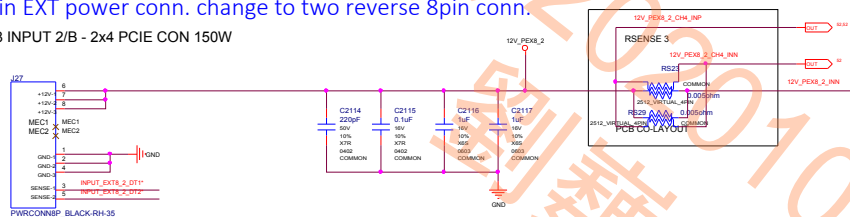
AND GATE LOGIC				SWITCH		VOUT
GPIO1	GPIO29	SWITCH	VOUT	SWITCH		VOUT
0	0	0	12V	0	VOUT = VIN1	
0	1	0	12V	1	VOUT = VIN2	
1	0	0	12V			
1	1	1	3V3A			



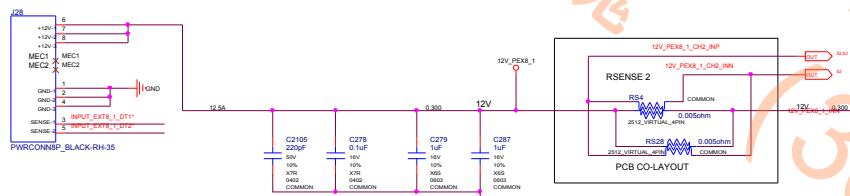
Remove 12 PIN Power connector's CURRENT STREAMING

one 12pin EXT power conn. change to two reverse 8pin conn.

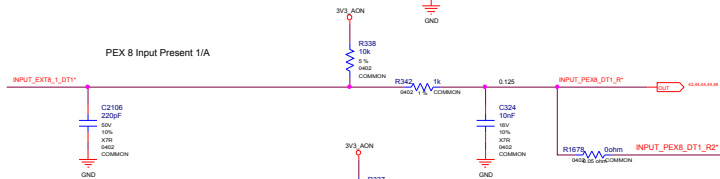
PEX8 INPUT 2/B - 2x4 PCIE CON 150W



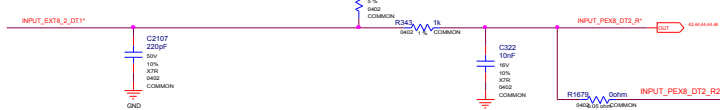
PEX8 INPUT 1/A - 2x4 PCIE CON 150W



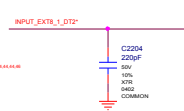
PEX 8 Input Present 1/A



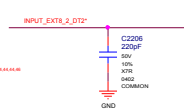
PEX 8 Input Present 2/B



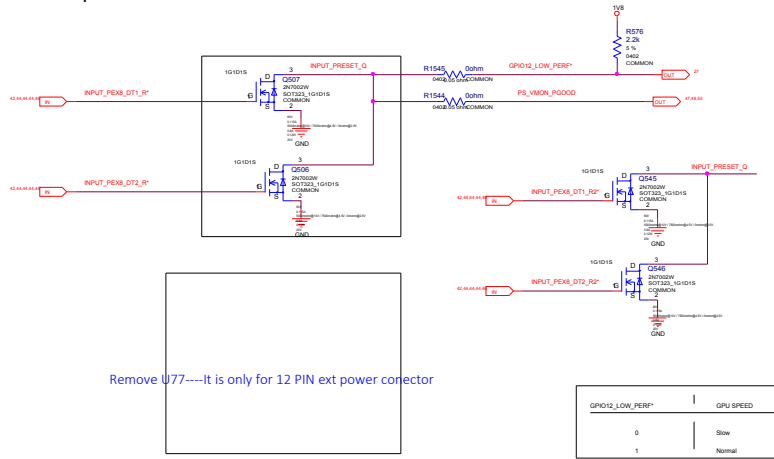
PEX 8 Input Present 1/A



PEX 8 Input Present 2/B



PEX Input - Under Power Boot Control

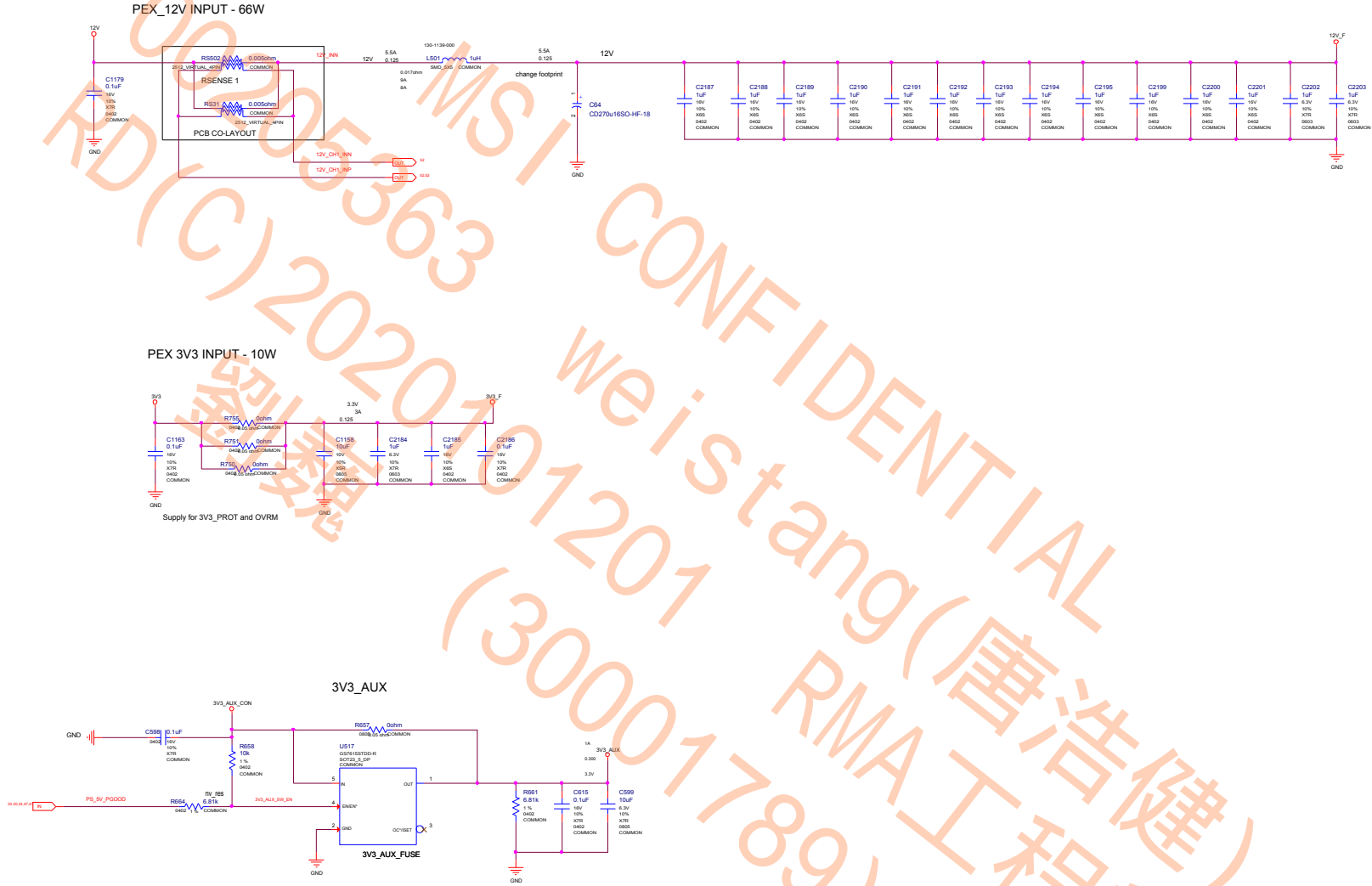


Remove U77----It is only for 12 PIN ext power connector

Remove RS24

one 12pin EXT power conn. change to two reverse 8pin conn.





POWER CONNECTOR HOT-UNPLUG DETECT

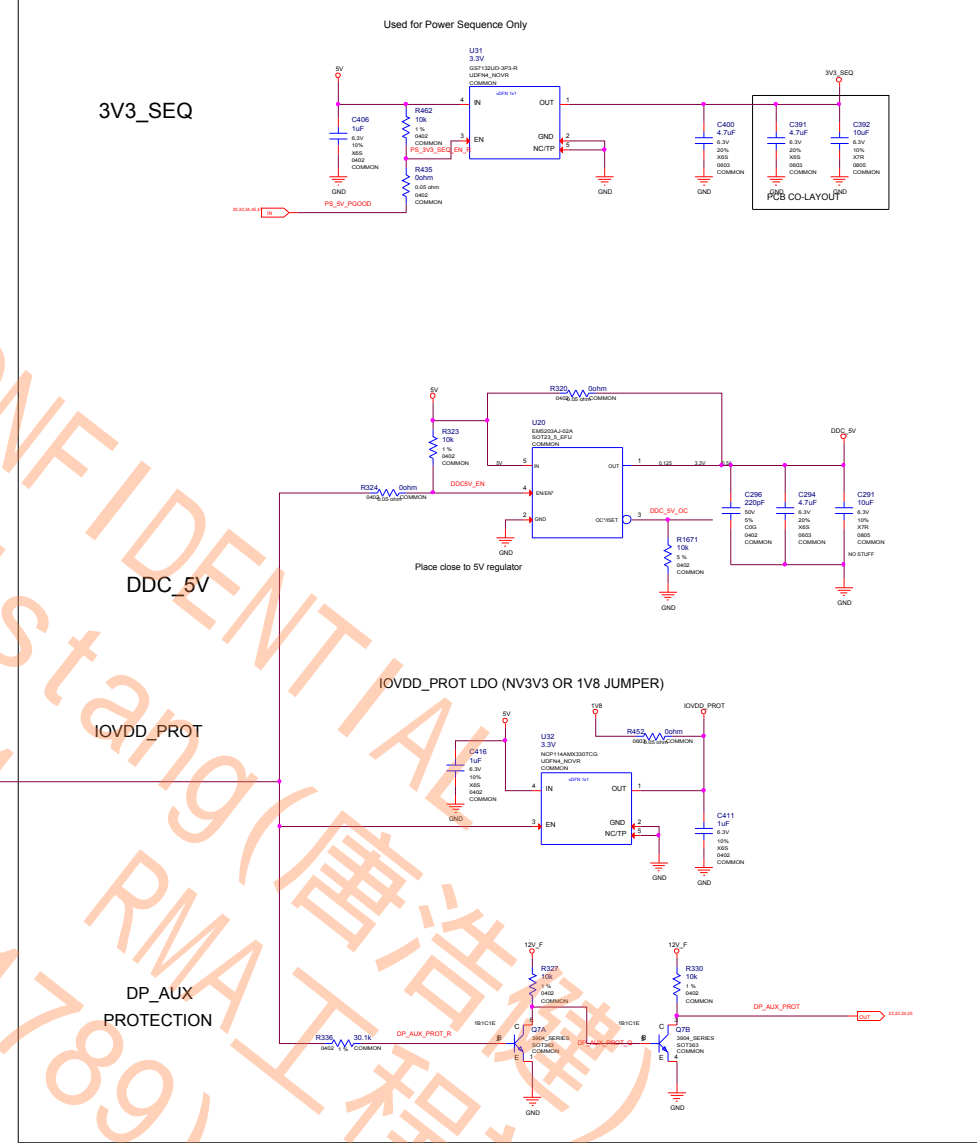
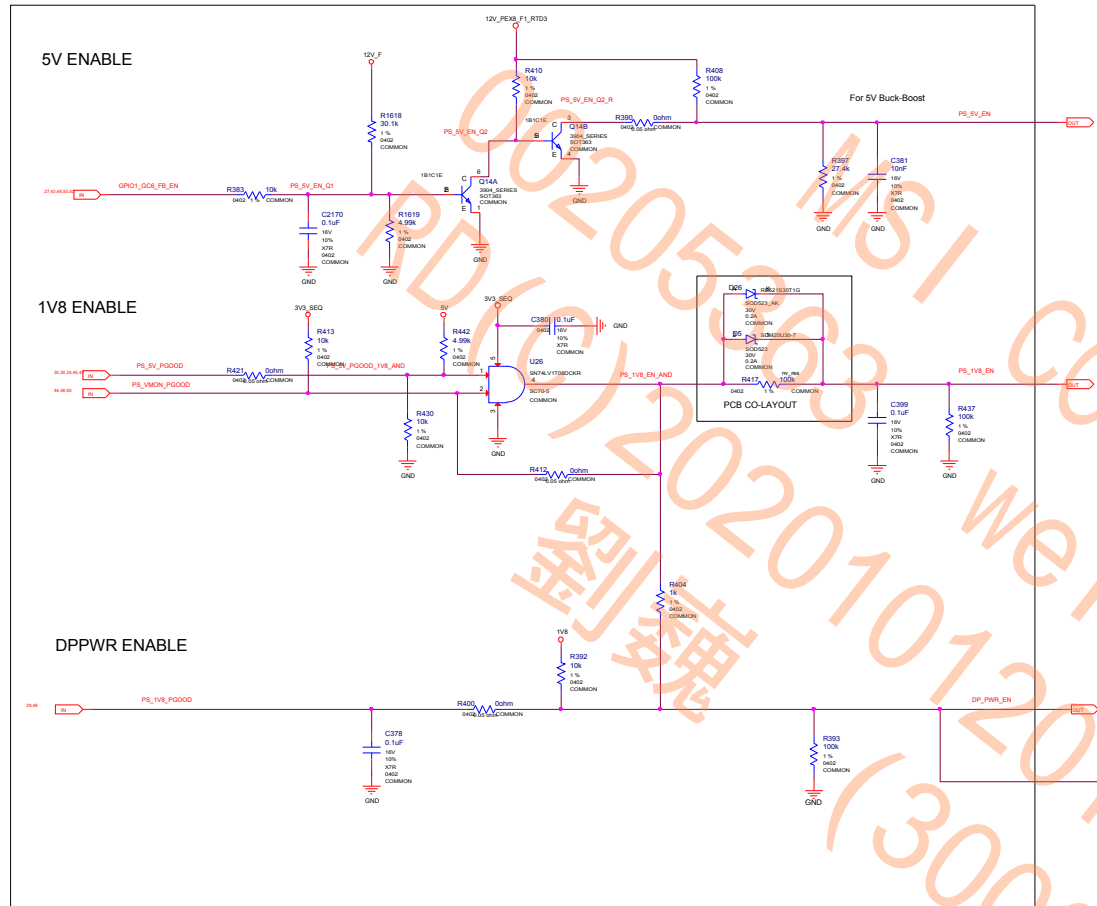
Remove power connector HOT-UNPLUG

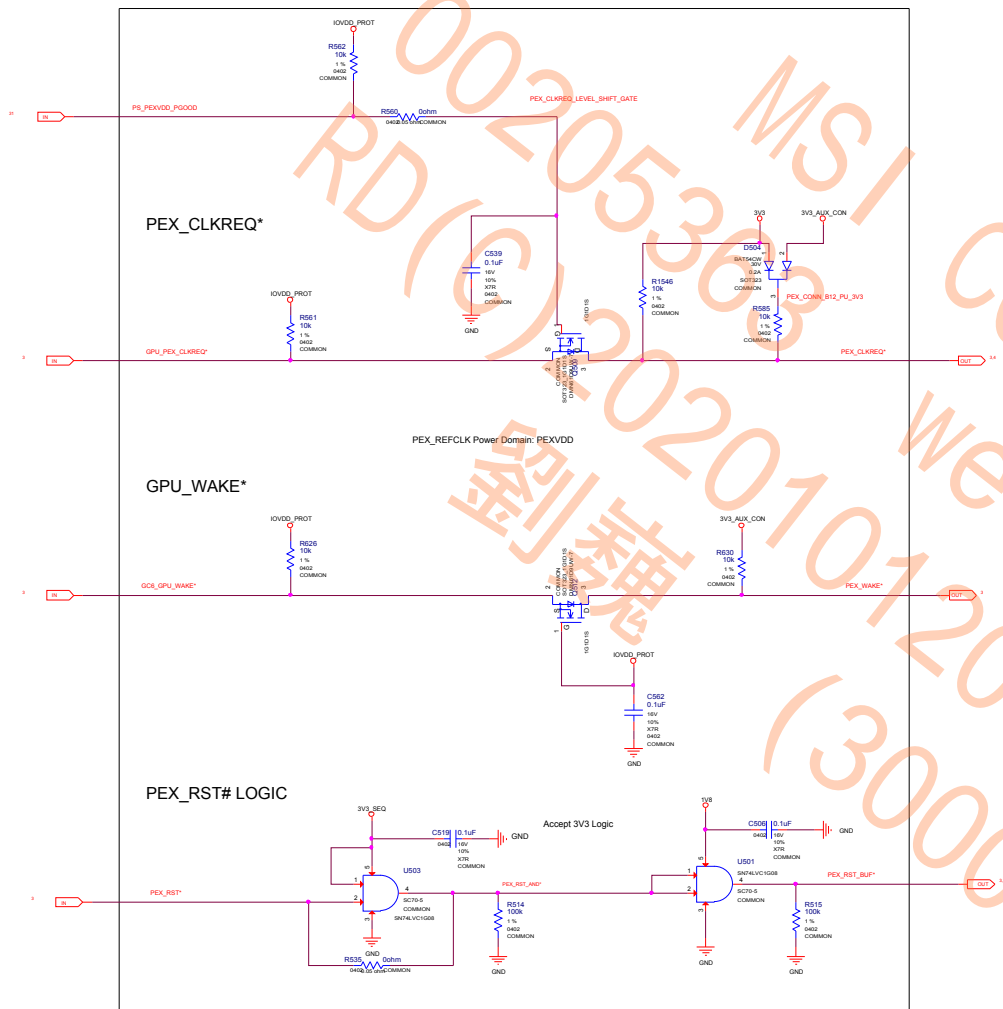
one 12pin EXT power conn. change to two reverse 8pin conn.

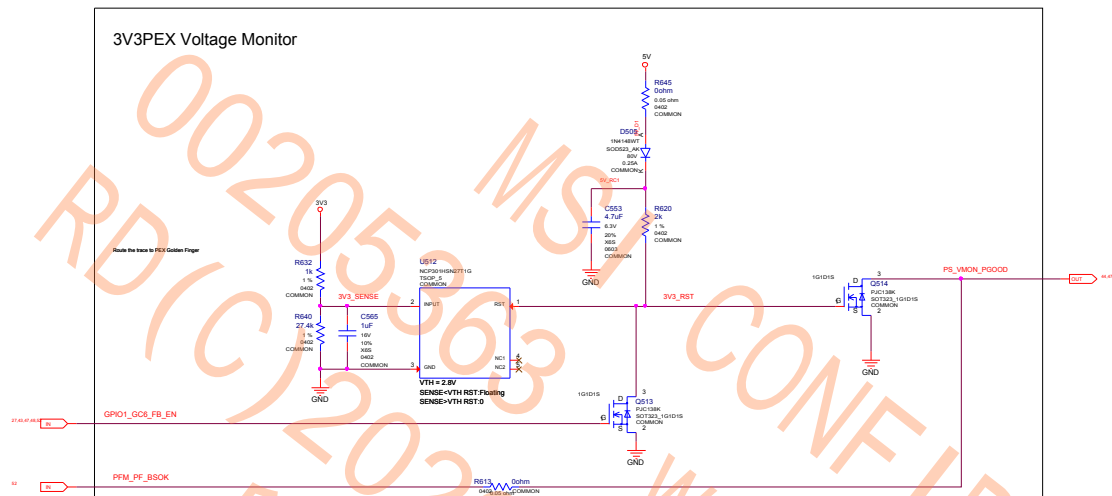
12V CURRENT STEERING (UNDER POWER BOOT):

GUIDES CURRENT FROM PEX EDGE TO PEX 8 PIN INPUT AREA

Remove 12V CURRENT STEERING







BOM Configuration

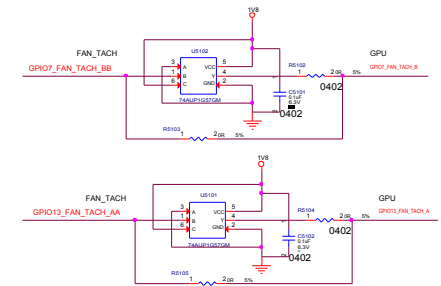
OPTIONS	PEX3V3_SENSE	PEX12V_SENSE	OTHER_12V_SENSE
Use Pre-Filter	Pre-Filter	Pre-Filter	Pre-Filter
NO Pre-Filter	Voltage_Monitor	Voltage_Monitor	N/A

Signal	Direction	Function
3V3	INPUT	Sense the 3V3 Voltage from PCIe golden finger
12V	INPUT	Sense the 12V Voltage from PCIe golden finger
PS_VMON_PGOD0	OPEN-DRAIN	Floating(H) once both 3V3 and 12V reach Vth
GC8_FB_EN		Indicator for RTD3/GC8 resistance, Use to Mask the VMON_PGOD0
PS_PF_SKIP	INPUT	From INA3221(VPU) or Pre-filter(SKIP)
PS_PF_BSC0K	INPUT	From INA3221(PV) or Pre-filter(BS_OK)

2-PIN LED HEADER

Remove 2-PIN LED function

Add Fan tach buffer circuit



DUAL FAN 4-PIN HEADERS

change to D01-AT54C49-LA9

R_1 rename to RS106

R_3 rename to RS101

change J15 footprint

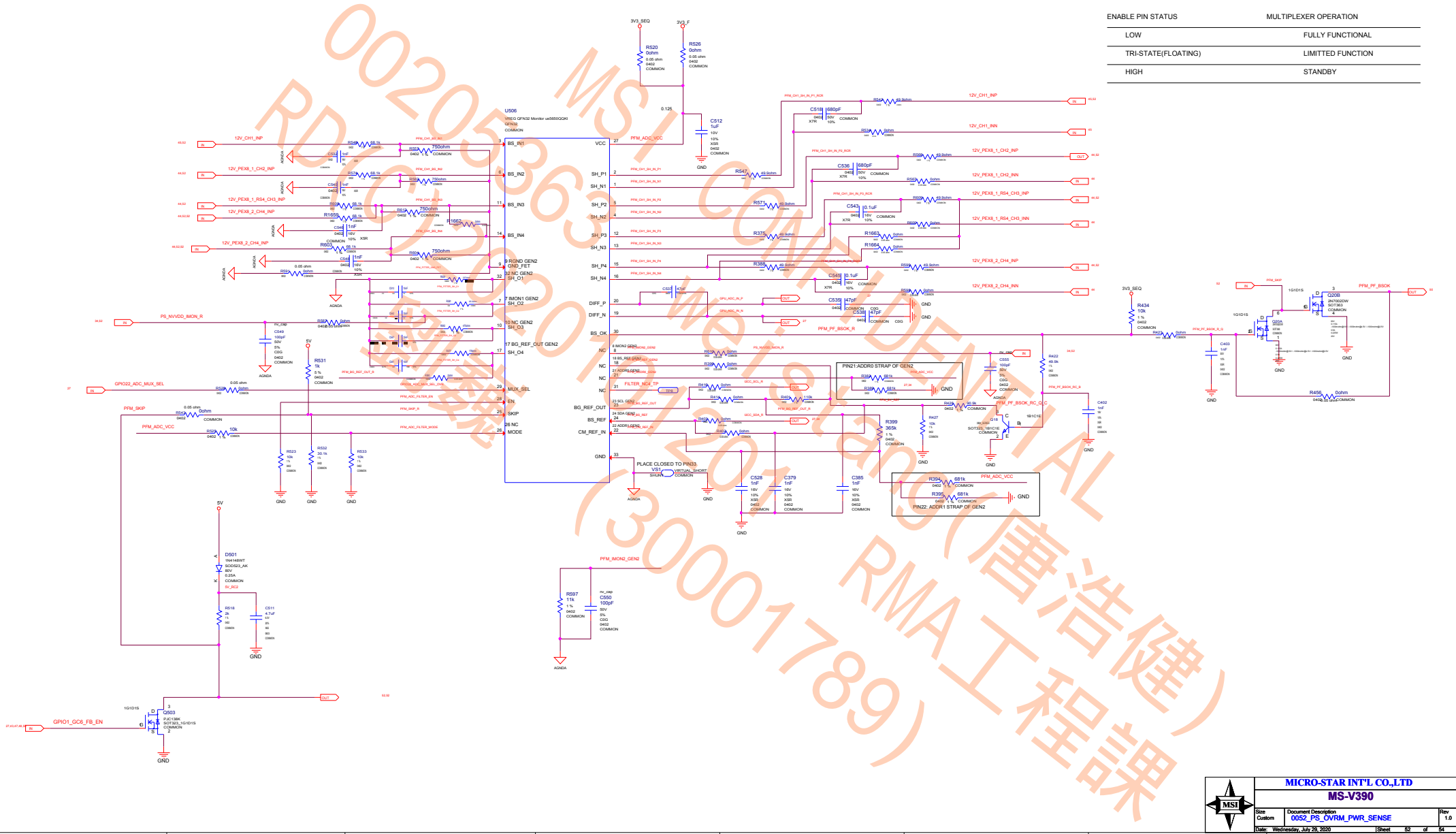
Remove J502

change J16 footprint

Remove J501

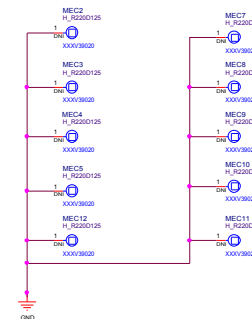
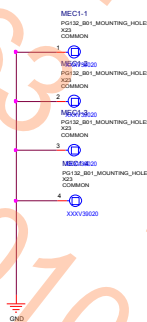
ENABLE PIN STATUS		MULTIPLEXER OPERATION	
LOW		FULLY FUNCTIONAL	
TRI-STATE(FLOATING)		LIMITED FUNCTION	
HIGH		STANDBY	

ENABLE PIN STATUS		MULTIPLEXER OPERATION	
LOW		FULLY FUNCTIONAL	
TRI-STATE(FLOATING)		LIMITED FUNCTION	
HIGH		STANDBY	



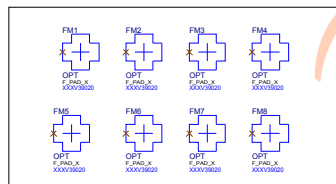
Bracket:

Add MEC (from V388 10)

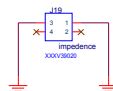
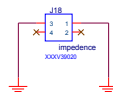


ADD MEC

Remove CLP1 / CLP2 / CLP3



Bracket Screw



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